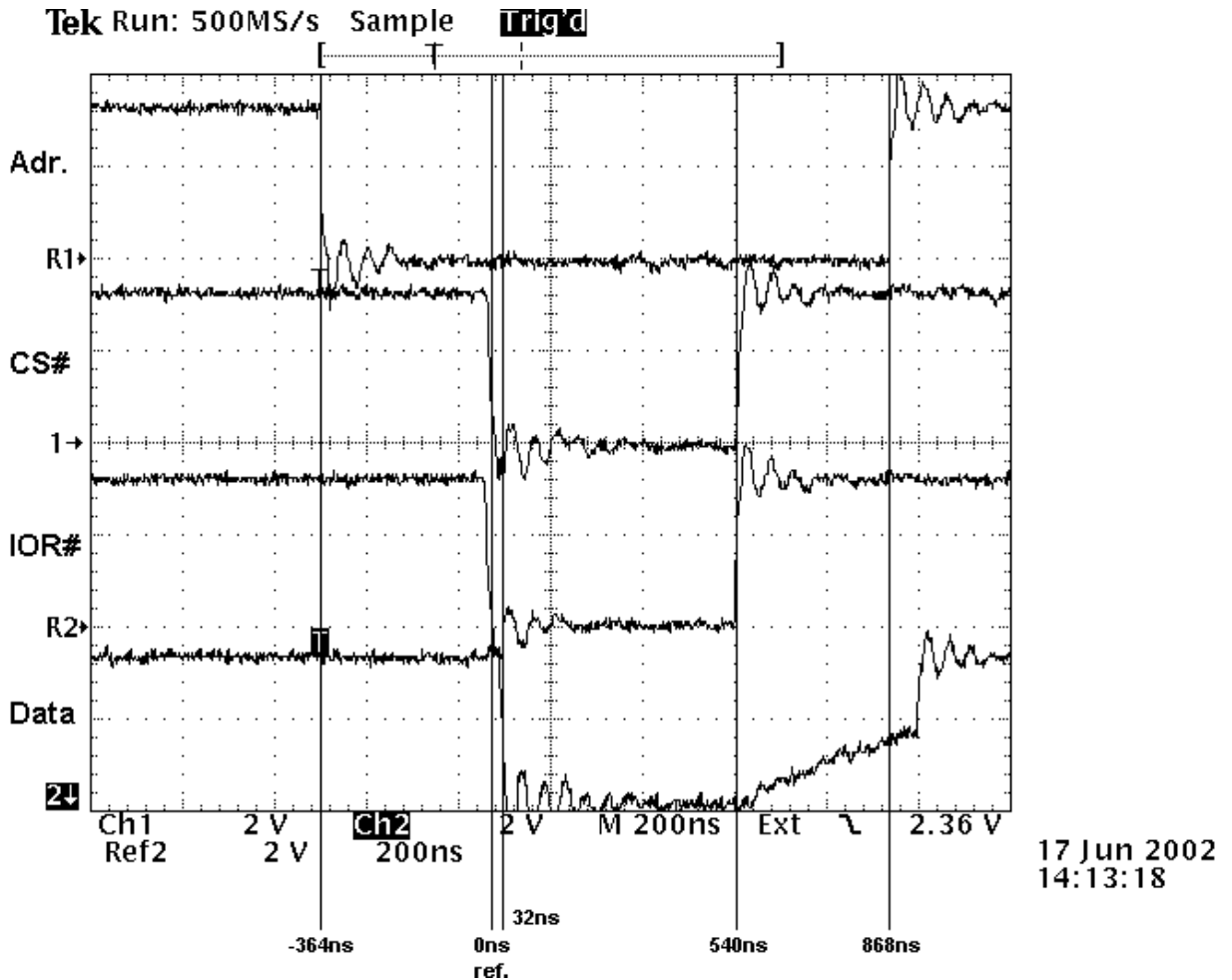


I/O Extension Bus Read and Write Timing Diagrams

- 1. Diagram: Read external I/O device with 33 MHz clock speed.

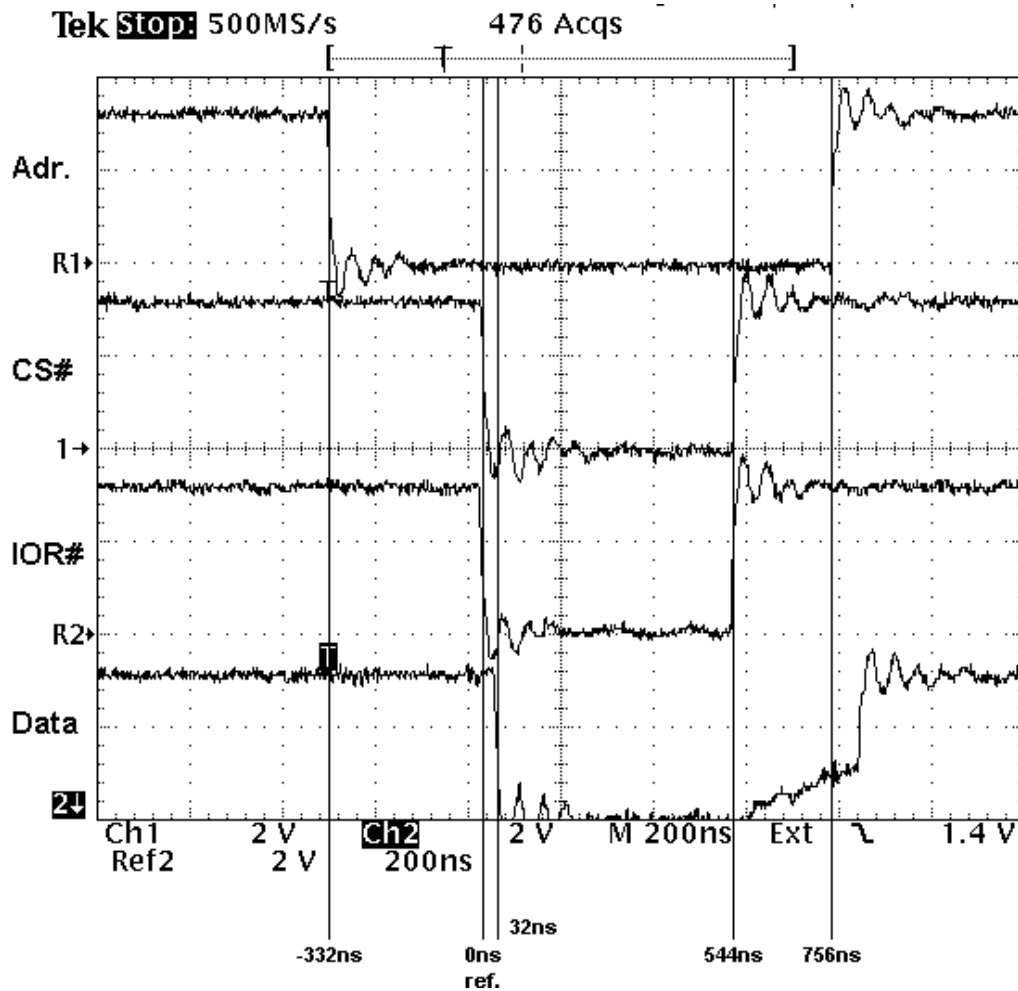


The bus load is a 82C55 PIO (parallel I/O) interface circuit. The timing is the same for all (A)DNP/1486 clock speeds.

Parameter	Typ. Value	Units
Address Prefetch Time	360	nsec.
Chip Select Pulse Width	540	nsec.
Address Hold Time	330	nsec.
Read Cycle Time	1230	nsec.
Write Cycle Time	1350	nsec.
Data Prefetch Time	300	nsec.
Data Hold Time	100	nsec.

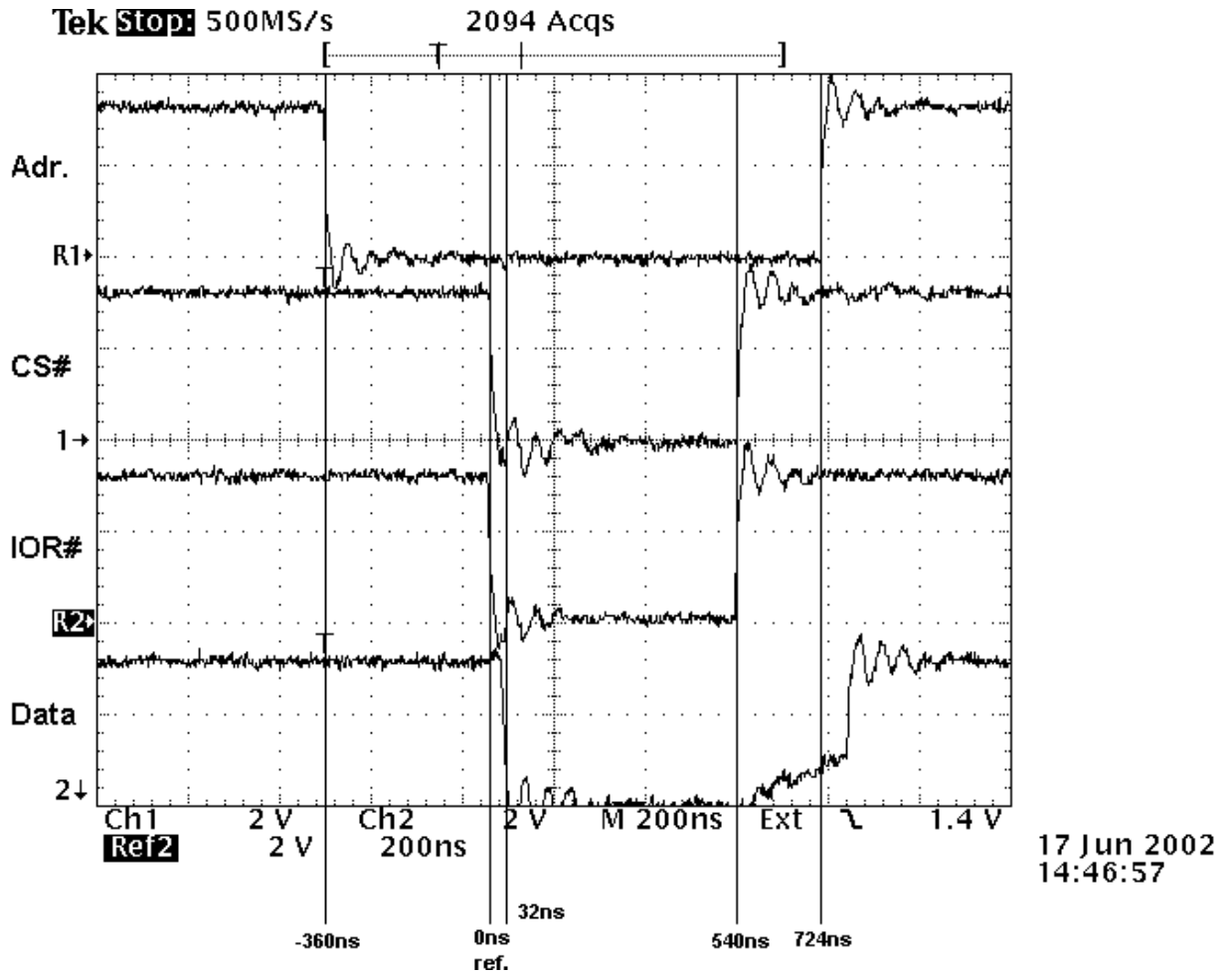
Table 1: Timing Values for I/O Read and Write Cycles

- **2. Diagram:** Read external I/O device with 66 MHz clock speed.

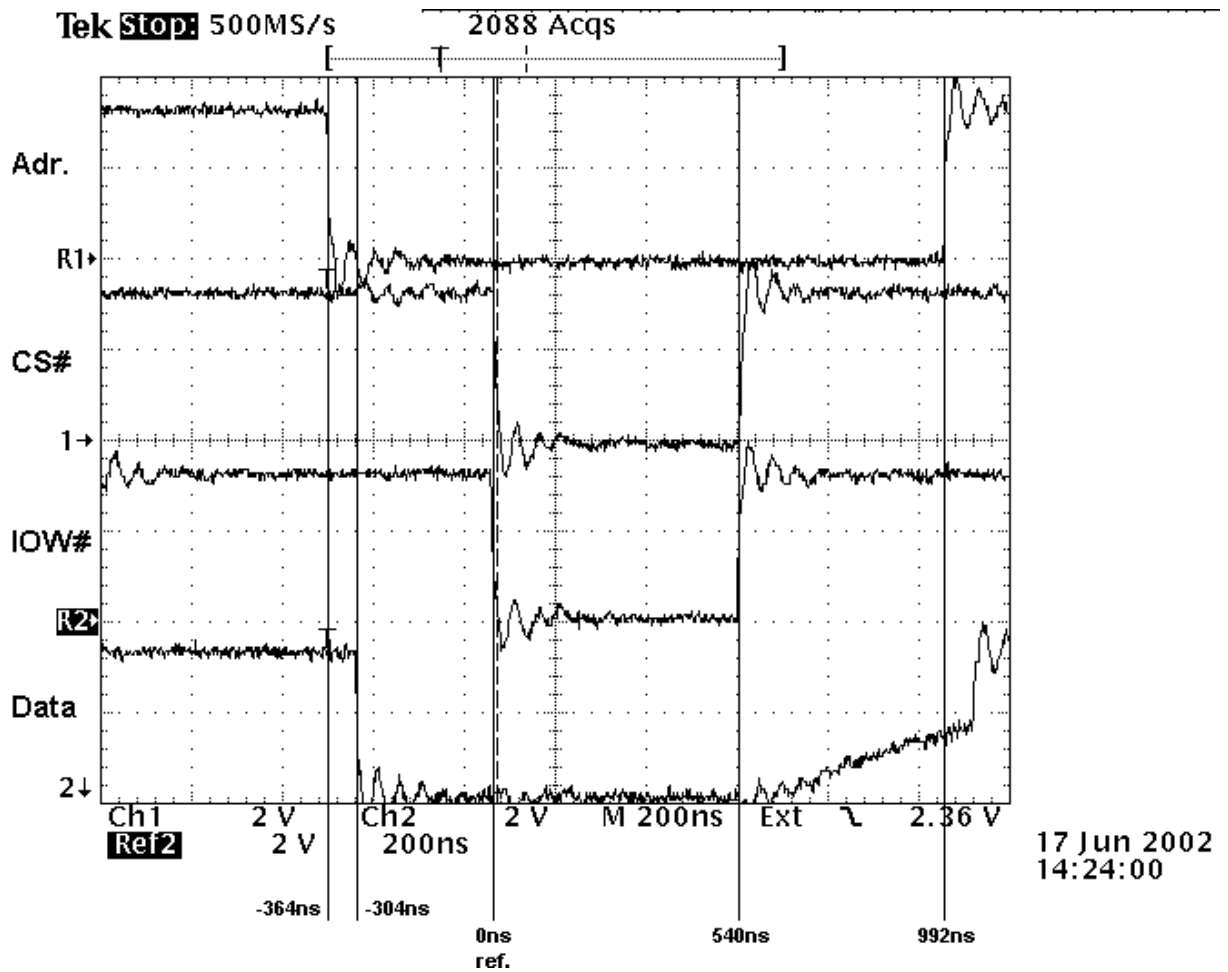


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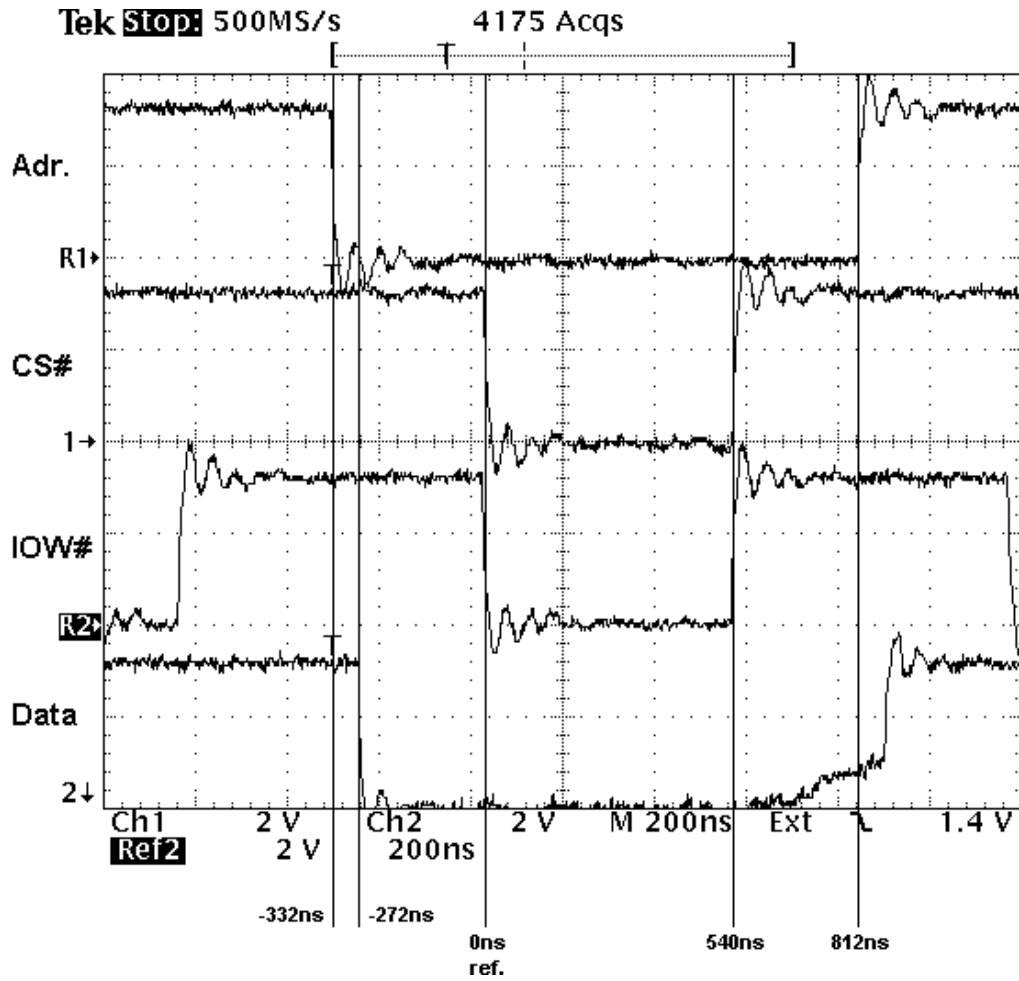
- 3. Diagram: Read external I/O device with 100 MHz clock speed.



- 4. Diagram: Write external I/O device with 33 MHz clock speed.

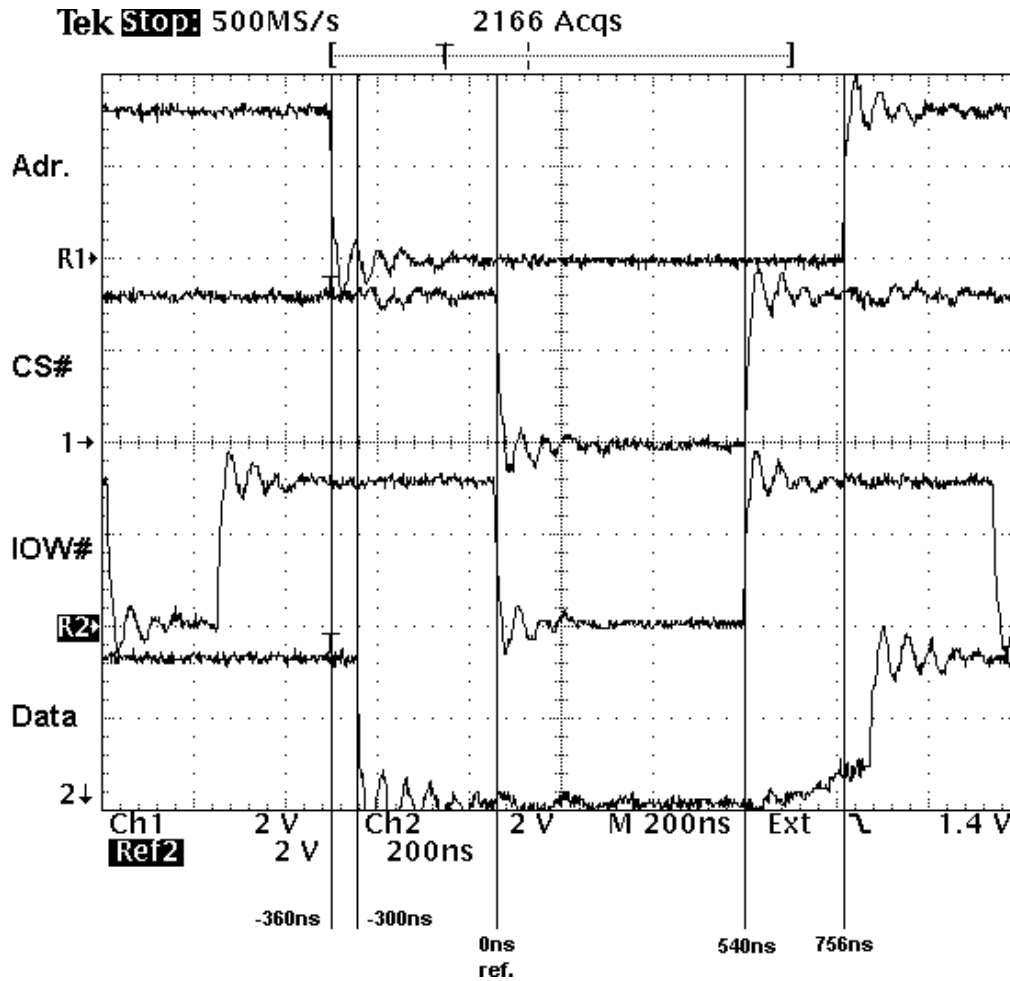


- **5. Diagram:** Write external I/O device with 66 MHz clock speed.



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- **6. Diagram:** Write external I/O device with 100 MHz clock speed.



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