

How to understand the DNP/9200 PIO signal mapping

The bits of the DNP/9200 20-bit PIO (Parallel Input Output Port) are directly connected to signals of the Atmel AT91RM9200 ARM920T-based 32-bit microcontroller. The following table shows this mapping. Please see also the *Atmel AT91RM9200 User Manual* and *Atmel AT91RM9200 Summary* for the AT91RM9200 pin function details

Pin	Name	AT91RM9200 Pin Function	AT91RM9200 Pin 256-ball BGA Package
1	PA0	GPIO PB6	C9
2	PA1	GPIO PB7	E9
3	PA2	GPIO PB8	D9
4	PA3	GPIO PB9	A8
5	PA4	GPIO PB10	B8
6	PA5	GPIO PB11	F9
7	PA6	GPIO PC1	M14
8	PA7	GPIO PC3	L15
9	PB0	GPIO PA27	G12
10	PB1	GPIO PA28	F12
11	PB2	GPIO PA29	E11
12	PB3	GPIO PB3	E10
13	PB4	GPIO PB4	A9
14	PB5	GPIO PB5	B9
15	PB6	GPIO PA5	F16
16	PB7	GPIO PA6	E15
17	PC0	GPIO PA1	G14
18	PC1	GPIO PA0	G13
19	PC2	GPIO PA2	G15
20	PC3	GPIO PA3	G16

Please note: Most of the DNP/9200 PIO pins offers alternate functions. Please see the Atmel manuals for more details.