

EXT\_CS#0..3] EXT\_CS#0..3]  
SA0..25] SA0..25]  
UART\_CLK] UART\_CLK]  
EXT\_INT1..5] EXT\_INT1..5]  
SIO2 [1..9] SIO2 [1..9]  
COM1 [1..9] COM1 [1..9]  
Port\_C0..3] Port\_C0..3]  
Port\_A0..7] Port\_A0..7]  
Port\_B0..7] Port\_B0..7]  
SD0..31] SD0..31]  
MISC0..26] MISC0..26]  
RESET#] RESET#]

